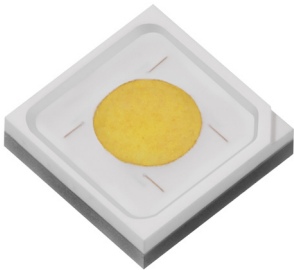


SFT-25R-WG (CRI > 90)

High Power White LEDs



Features

With Luminus' advanced chip and packaging technologies, SFT-25R-W maximizes the Candela/Lumen K-Factor for high optical performance in directional lighting:

- A small emitter enables high intensity, long beam distance and small optics.
- A patented round emitter, in comparison with a square emitter, enables better uniformity and higher intensity in beam spots.
- A flat window, in comparison with a domed cover, allows the optics to be closer to the emitter to achieve higher intensity and longer beam distance.

In addition, Luminus phosphor-on-chip technology's high uniformity of color over radiation angle enables superior beam spot light quality.

- Package Size: 3535
- Maximum Drive Current: 5 A
- Color Temperature: 5700K
- Color Rendering Index: > 90
- Low thermal resistance: 0.8 °C/W
- ANSI-compatible chromaticity bins
- Electrically isolated thermal path
- 8 kV HBM ESD rating per ANSI/ESDA/JEDEC JS-001

Applications

- Stage and Studio Lighting
- Stadium and Sports Field Lighting
- Roadway and Outdoor Lighting
- Landscape Lighting
- Garage and Canopy Lights
- High / Low Bay Industrial Lighting
- Track Light
- Spotlight
- Wall Washer
- Task Light

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Ordering Information

Ordering Part Numbers¹

CRI	CCT	Luminous Flux		Ordering Part Number		
		Minimum Flux Bin ¹	Minimum Flux ²	Chromaticity Bin Kit ANSI Centers	Chromaticity Bin Kit 3-Step Ellipse	Chromaticity Bin Kit 5-Step Ellipse
> 90	5700K	F2	440 lm	SFT-25R-WG57-AV2-F2572	SFT-25R-WG57-AV2-F257E3	SFT-25R-WG57-AV2-F257E5
		F3	465 lm	SFT-25R-WG57-AV2-F3572	SFT-25R-WG57-AV2-F357E3	SFT-25R-WG57-AV2-F357E5

Part Number Nomenclature

SFT	25R	W<xyy>	AV2	<ffcccc>
Product Family	Chip Area	Color	Package Configuration	Bin Kit
S: Surface Mount F: Flat Window T: Single Emitter	25: 2.5 mm ² R: Round Shape	W: White <x> CRI Category Code G: CRI>90 <yy> Color Temperature 57: 5700K	AV2: 3535 package code	<ff> Minimum Flux Bin, see 'Luminous Flux Bins' table for details <cccc> Chromaticity Bin Kit, see 'Chromaticity Bin Kit Codes' table for details

Notes:

1. The Ordering Part Number specifies the Minimum Flux Bin in shipment; higher flux bins may be shipped without advance notice. Please refer to 'Luminous Flux Bins' table for details of all flux bins.
2. Product test condition: $I_f = 1500 \text{ mA}$, $T_j = 85^\circ\text{C}$.
3. Shipments always adhere to the color bins specified in each Chromaticity Bin Kit. See 'Chromaticity Bin Kit Codes' table for the color bins included in each bin kit.



Binning Structure

Luminous Flux Bins^{1,2}

Flux Bin	Binning @ 1500 mA			Correlated Minimum Flux (lm) @ $T_j=85^{\circ}\text{C}^2$			
	$T_j=85^{\circ}\text{C}$		$T_j=25^{\circ}\text{C}$				
	Minimum Flux (lm)	Maximum Flux (lm)	Minimum Flux (lm)	1000 mA	3000 A	4000 A	5000 mA
F1	415	440	465	295	718	884	1021
F2	440	465	493	312	761	937	1082
F3	465	490	521	330	804	990	1144
F4	490	520	549	348	848	1044	1205

Forward Voltage Bins³

Voltage Bin	Binning @ 1500 mA, $T_j=85^{\circ}\text{C}$	
	Minimum Voltage (V)	Maximum Voltage (V)
VJ	2.7	2.9
VK	2.9	3.1
VL	3.1	3.3

Notes:

- LEDs are measured at 25°C ambient temperature with 1500 mA 20 ms single pulse. The measured values are correlated to values at 85°C junction temperature (T_j). Luminus maintains a $\pm 6\%$ tolerance on flux measurement.
- Flux values at other junction temperature (T_j) and/or forward current conditions are calculated and for reference only.
- Individual voltage bins are not orderable.



Binning Structure

Chromaticity Binning Coordinates (ANSI Centers)

CCT	Bin	CIEx	CIEy	Bin	CIEx	CIEy	Bin	CIEx	CIEy	Bin	CIEx	CIEy
5700K	CA	0.3215	0.3350	CB	0.3207	0.3462	CC	0.3290	0.3538	CD	0.3290	0.3417
		0.3290	0.3417		0.3290	0.3538		0.3376	0.3616		0.3371	0.3490
		0.3290	0.3300		0.3290	0.3417		0.3371	0.3490		0.3366	0.3369
		0.3222	0.3243		0.3215	0.3350		0.3290	0.3417		0.3290	0.3300

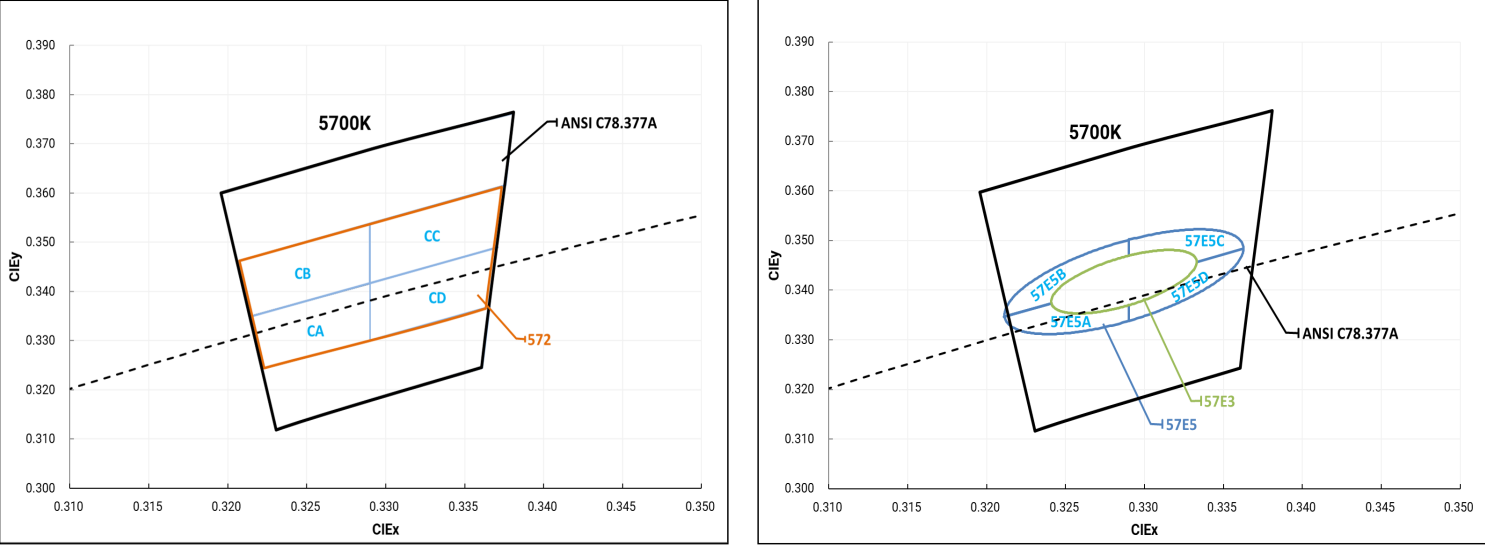
Chromaticity Binning Coordinates (3-Step & 5-Step Ellipse)

CCT	Center Point		3-Step Ellipse		5-Step Ellipse		Angle
	CIEx	CIEy	a	b	a	b	
5700K	0.3287	0.3417	0.0072	0.0032	0.0119	0.0052	59.09°



Binning Structure

Chromaticity Binning Diagram^{1,2}



Chromaticity Bin Kit Codes

CCT	Bin Kit	Chromaticity Bins
5700K	572	CA, CB, CC, CD
	57E5	57E3, 57E5A, 57E5B, 57E5C, 57E5D

Notes:

- 1. LED chromaticity is measured and binned at 25°C ambient temperature with 1500 mA 20 ms single pulse.
- 2. Luminus maintains a tolerance of ±0.005 on Chromaticity (CIE_x, CIE_y) measurement.



Absolute Maximum Ratings

Parameter		Symbol	Values	Unit
Forward Current	Minimum	$I_{f \min}$	0.1	A
	Maximum	$I_{f \max}$	5.0	
Surge Current ($t < 10$ ms, Duty Cycle $< 10\%$)		I_s	6.0	
Power Dissipation		P_D	18	W
Reverse Voltage (@ $I_f = 10$ mA)		V_r	5	V
Junction Temperature		T_j	150	°C
Operating Temperature Range		T_{opr}	-40 to 100	
Storage Temperature Range		T_{stg}	-40 to 100	
ESD withstand Voltage ANSI/ESDA/JEDEC JS-001 (HBM)		V_{ESD}	8	kV
ESD withstand Voltage ANSI/ESDA/JEDEC JS-002 (CDM)		V_{CDM}	1	

Characteristics

Parameter ($I_f = 1500$ mA, $T_j = 85^\circ\text{C}$)		Symbol	Value	Unit
Color Rendering Index ¹		CRI	> 90	
Forward Voltage	Minimum	$V_{f \min}$	2.7	V
	Typical	$V_{f \text{ typ}}$	2.9	
	Maximum	$V_{f \max}$	3.3	
Viewing Angle		$2\theta_{1/2}$	115	°
Temperature Coefficient of Voltage		$\partial V_f / \partial T$	-1.4	mV/°C
Electrical Thermal Resistance (Junction to Solder Point) ²		$R_{th \text{ JS elec}}$	0.8	°C/W

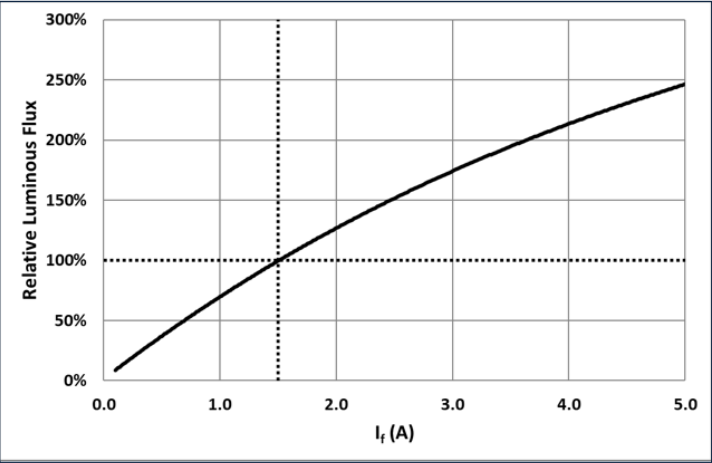
Notes:

1. Luminus maintains a tolerance of ± 2 on Color Rendering Index (CRI) measurement.
2. Thermal measurements are in accordance with JEDEC 51-14.



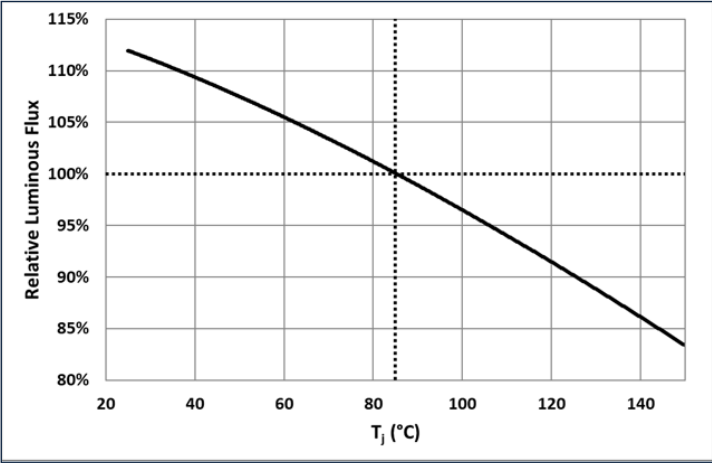
Relative Luminous Flux vs Forward Current

$T_j = 85^{\circ}\text{C}$



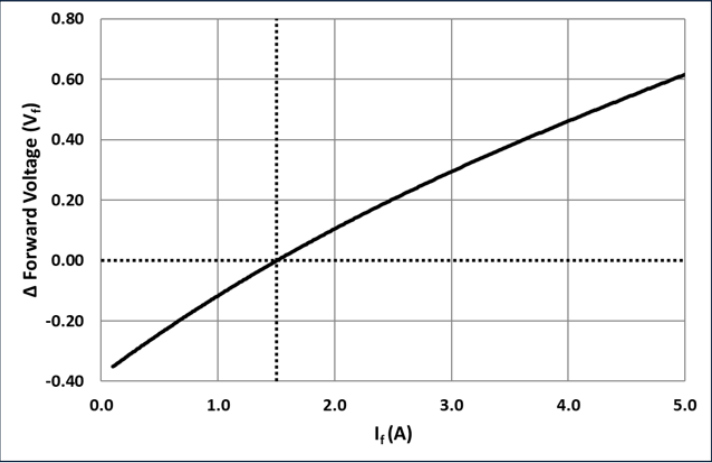
Relative Luminous Flux vs Temperature

$I_f = 1.5\text{ A}$



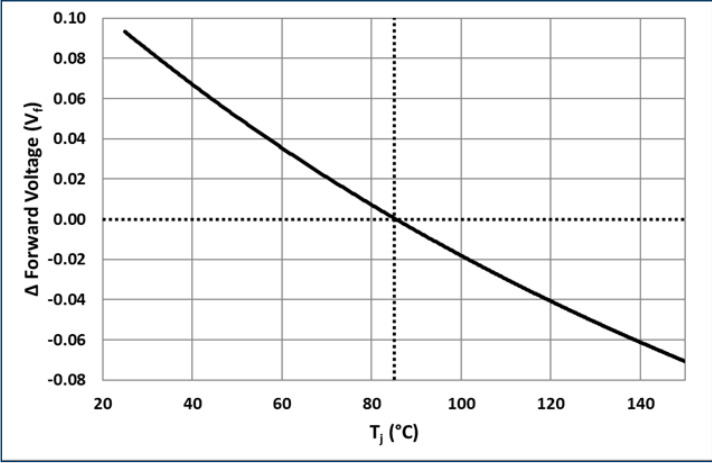
Forward Voltage vs Forward Current

$T_j = 85^{\circ}\text{C}$



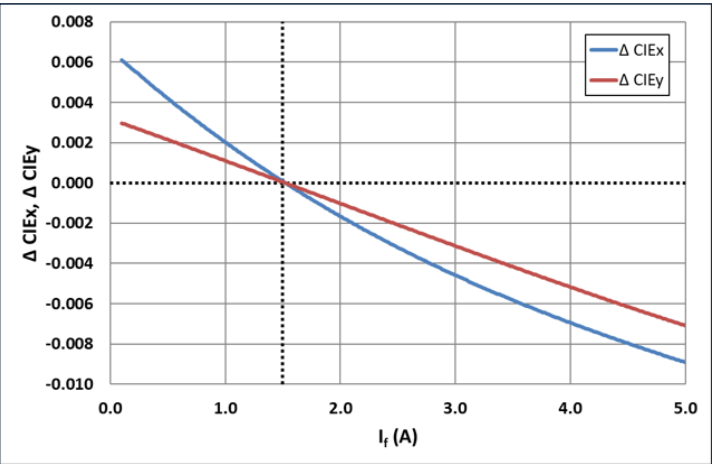
Forward Voltage vs Temperature

$I_f = 1.5\text{ A}$



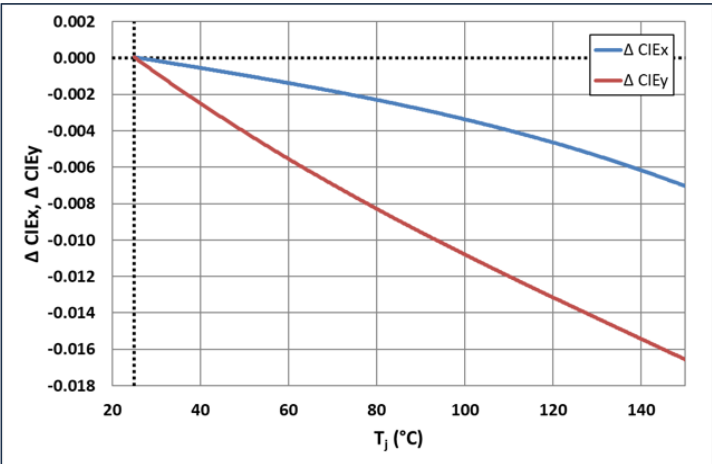
Relative Chromaticity vs Forward Current

$T_j = 85^{\circ}\text{C}$



Relative Chromaticity vs Temperature

$I_f = 1.5\text{ A}$

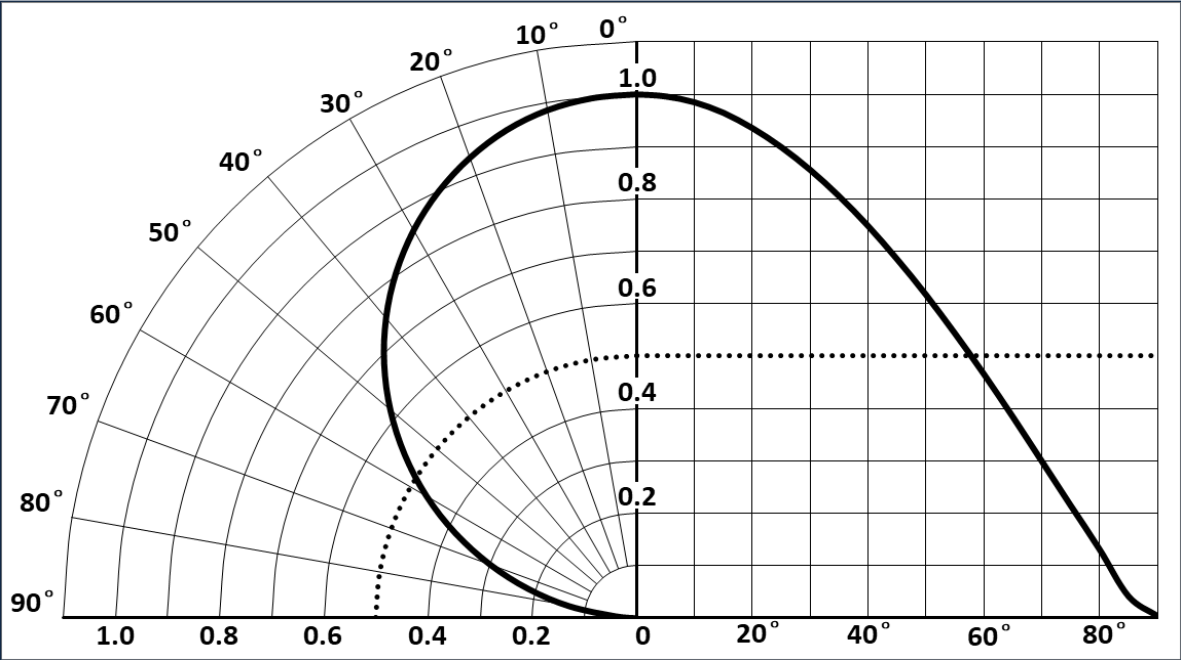




Angular Distribution and Typical Spectrum

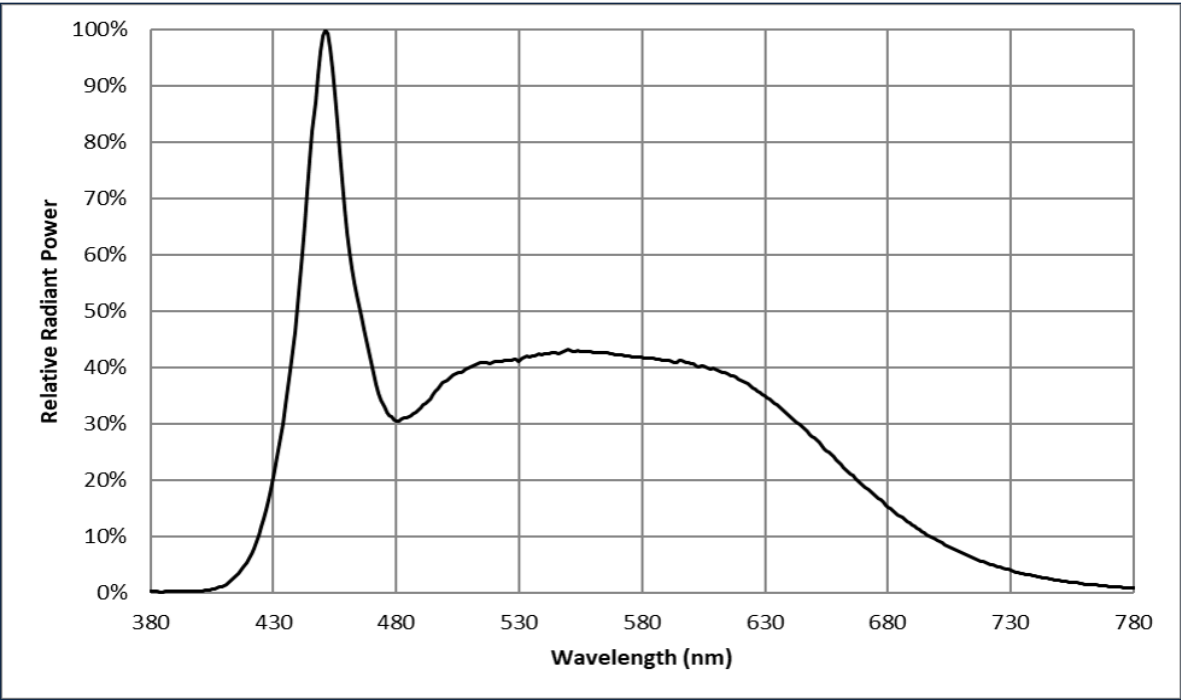
Angular Distribution

$I_f = 1.5\text{ A}$; $T_j = 25^\circ\text{C}$



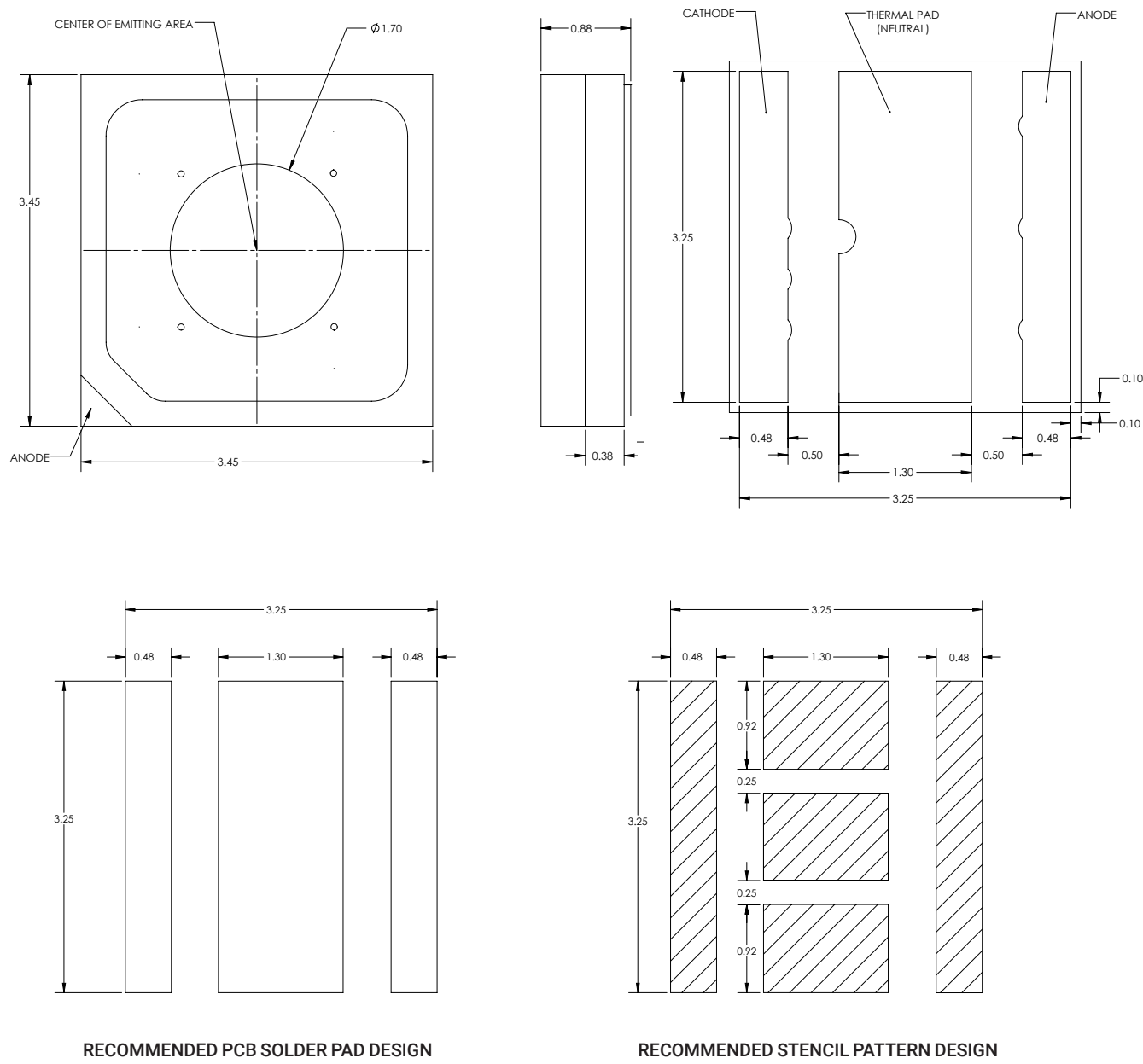
Relative Spectral Power Distribution

$I_f = 1.5\text{ A}$; $T_j = 85^\circ\text{C}$



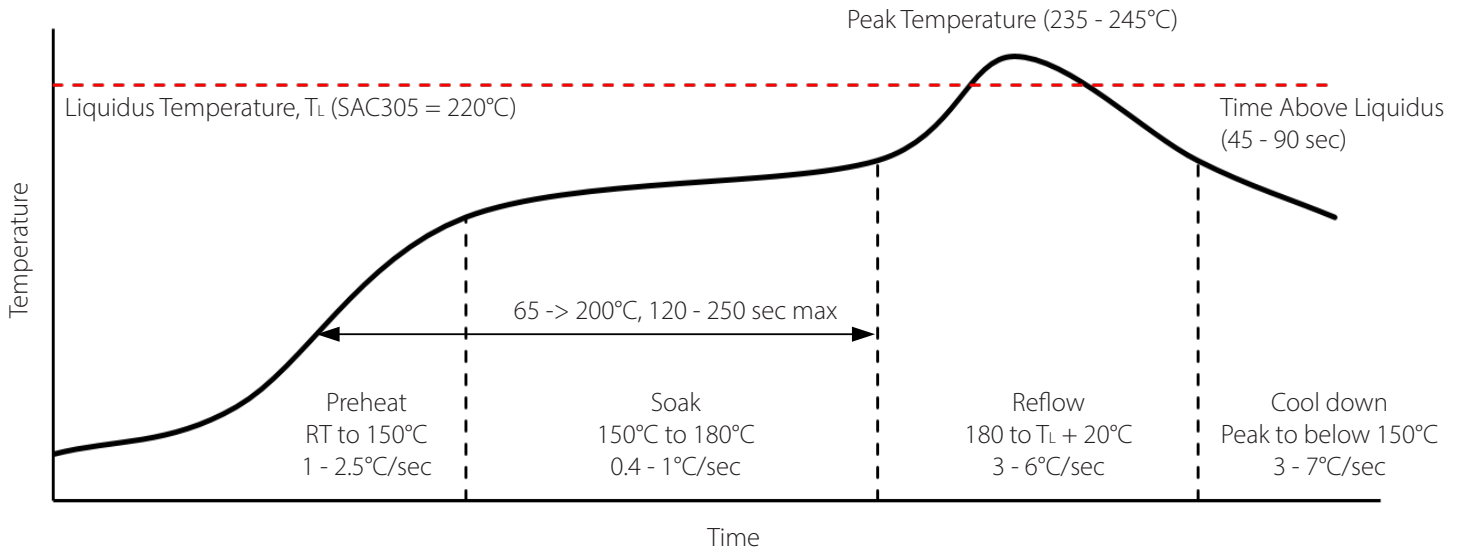


Mechanical Dimensions¹



Note:
1. All dimensions are in millimeter ± 0.13 mm.

Soldering Profile



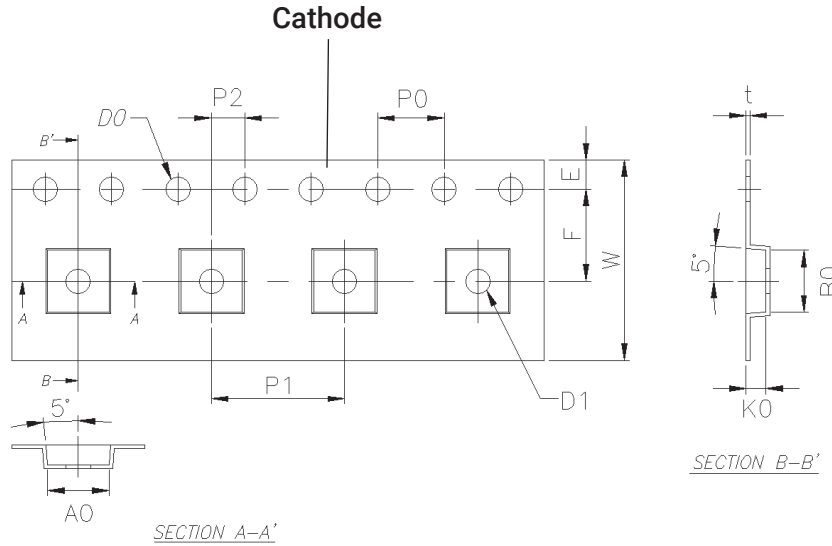
SMT Solder Rework Temperature Guidelines

Parameter	Manual Hotplate Reflow	Hot Air Gun Reflow
Heating Time	< 60 sec	
Hotplate Temperature	< 245°C	< 150°C

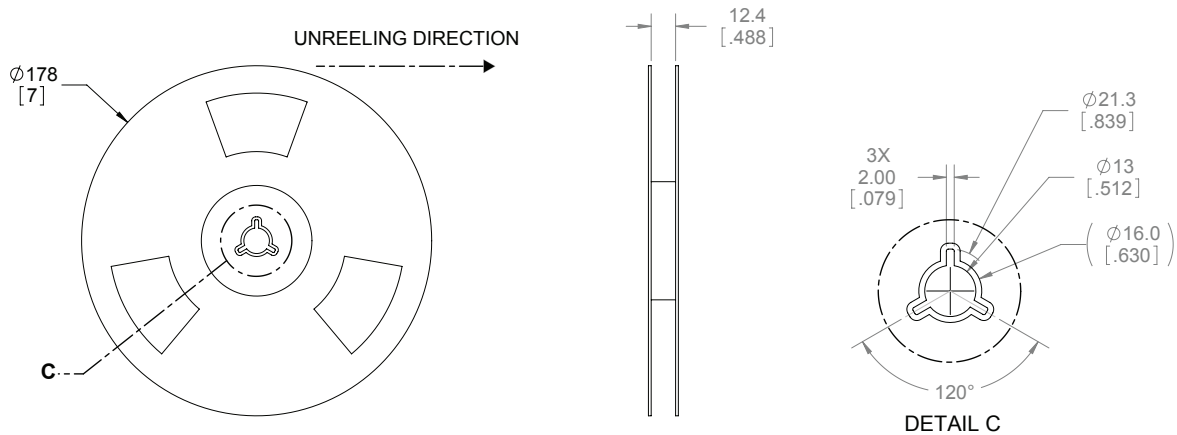
Notes:

- Product complies to Moisture Sensitivity Level 3 (MSL 3).
- The numbers in the table are specific to SAC305. Luminus recommends using an SAC305 solder paste with a no-clean flux for RoHS compliant products.
- During the pick and place process, axial forces on the dome (or window) should not exceed 0.5 Newtons (N).
- Use of a multi-zone IR reflow oven with a nitrogen blanket is recommended.
- Time-temperature profile of the reflow process showing the four functional profile zones are defined in IPC-7801. Temperature is referenced to the center of the PCB.
- Luminus recommends to use the solder paste data sheet information as a starting point in time-temperature process development.
- These are general guidelines. Consult the solder paste manufacturer's datasheet for guidelines specific to the alloy and flux combination used in your application. For more information, please refer to:
<https://luminusdevices.zendesk.com/hc/en-us/articles/360060306692-How-do-I-Reflow-Solder-Luminus-SMD-Components->
- For any technical questions about soldering process, please contact Luminus at techsupport@luminus.com.

Tape and Reel Outline



Parameter	Dimension (mm)
A0	3.70±0.10
B0	3.70±0.10
D0	1.50±0.10
D1	1.50±0.10
E	1.75±0.10
F	5.50±0.10
K0	1.20±0.10
P0	4.00±0.10
P1	8.00±0.10
P2	2.00±0.10
t	0.23±0.05
W	12±0.3



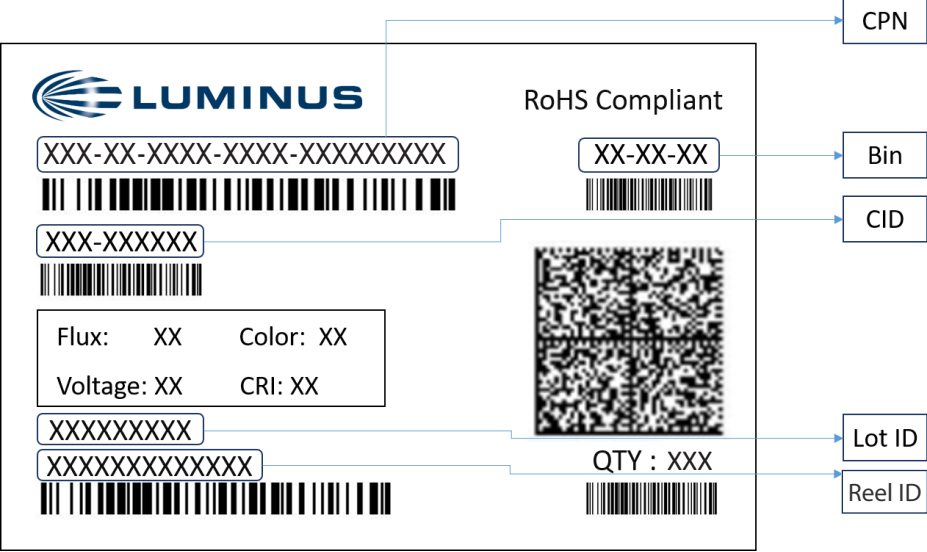
Notes:

1. Each reel contains 500 units of LEDs.
2. Black anti-static tape material (Denka ECM3/ECAP3)
3. The accumulated tolerance for ten chain holes should be no more than 0.2 mm.
4. The tortuosity of 250 mm tape should be no more than 1 mm.
5. Leave minimum 800 mm with empty compartments sealed by cover tape for lead in.
6. Leave minimum 1200 mm with empty compartments sealed by cover tape for trailer.
7. All dimensions must comply to EIA-481-D.
8. Final tape and reel packaging must meet the requirements of JEDEC -STD-033, LEVEL 2A.



Shipping Label

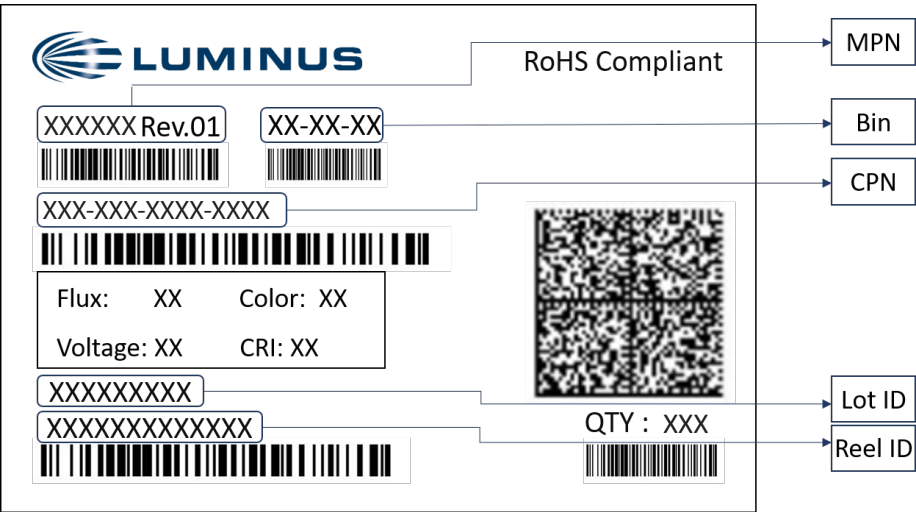
Label on Packaging Box



Label Fields:

- CPN:** Luminus ordering part number
- CID:** Customer's part number
- QTY:** Quantity of parts per reel
- Flux:** Bin as defined on page 3
- Voltage:** Bin as defined on page 3
- Color:** Bin as defined on page 4
- CRI:** NA
- Lot ID & Reel ID:** For Luminus internal use

Label on Reel



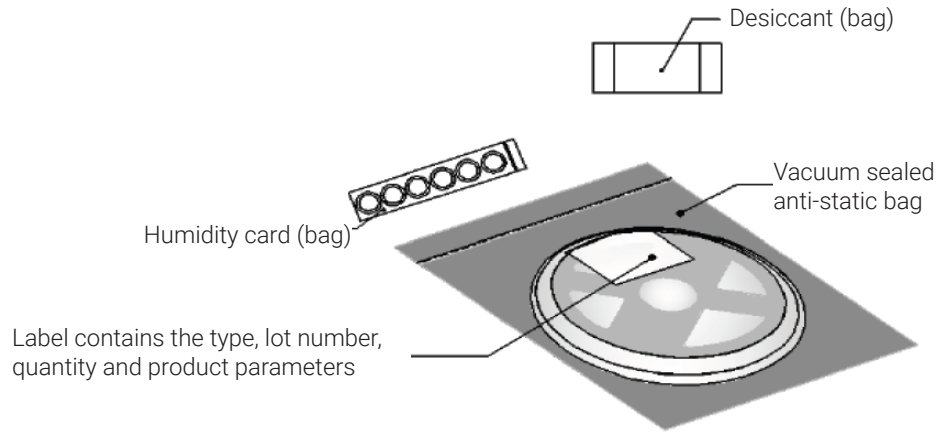
Label Fields:

- CPN:** Luminus ordering part number
- MPN:** For Luminus internal use
- QTY:** Quantity of parts per reel
- Flux:** Bin as defined on page 3
- Voltage:** Bin as defined on page 3
- Color:** Bin as defined on page 4
- CRI:** NA
- Lot ID & Reel ID:** For Luminus internal use



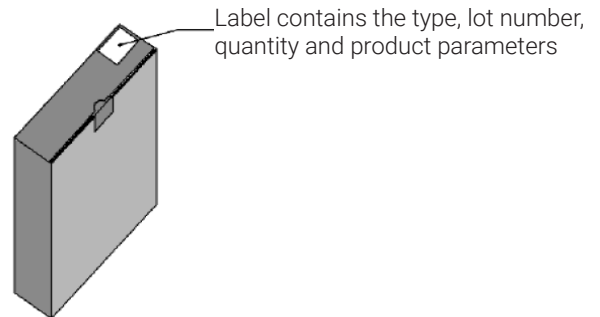
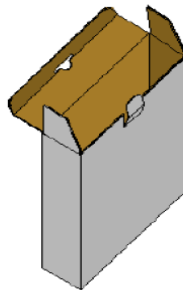
Packaging

Packaged Reel

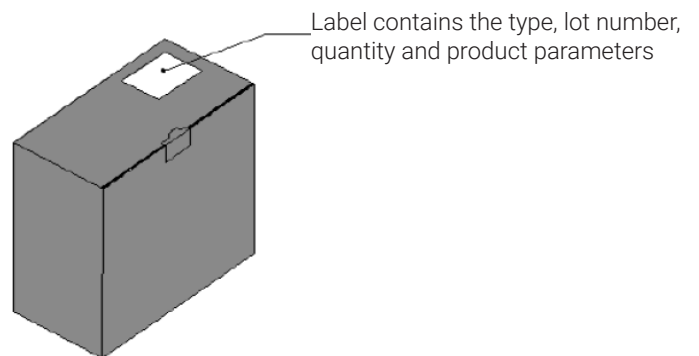
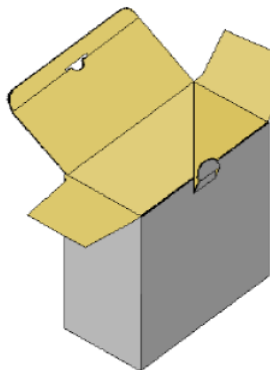


Packaging boxes

Box Size 1 - 5 reels per box
Size: 22.5 x 24.5 x 6.5 cm



Box Size 2 - 10 reels per box
Size: 22.5 x 24.5 x 13 cm



Packing Configuration:

- 500 units per reel
- Each reel is enclosed in anti-static bag
- Shipping label is placed on top of each reel
- Multiple labels are attached to the box (one label per reel inside the box)



Notes

Environmental Compliance

Luminus complies with RoHS and REACH. Luminus is committed to selling environmentally friendly and sustainable products. We do not use harmful or hazardous substances in our composites and products. Luminus will not intentionally add the following restricted materials to our products: lead, mercury, cadmium, hexavalent chromium, polybrominated biphenyls (PBB), or polybrominated diphenyl ethers (PBDE).

Static Electricity

1. The products are sensitive to static electricity, and care should be taken when handling them.
2. Static electricity or surge voltage will damage the LEDs. It is recommended to wear anti-electrostatic gloves or wristband when handling the LEDs.
3. All devices, equipment and machinery must be properly grounded. It is recommended that measures be taken against surge voltage to the equipment that mounts the LEDs.

Reference: [APN-002815](#) Electrical Stress Damage to LEDs and How to Prevent It

Storage

Please follow J-STD-033D guidance on safe storage and bake treatment.

1. Before opening the package

The LEDs should be kept at a temperature lower than 40°C and relative humidity lower than 90%. The LEDs should be used within a year. When storing the LEDs, moisture proof package with absorbent material (silica gel) is recommended.

2. After opening the package

The LEDs should be kept at a temperature lower than 30°C and relative humidity lower than 60%. The LEDs should be soldered within 168 hours (7 days) after opening the moisture proof package.

If unused LEDs remain, they should be stored in moisture proof packages, such as sealed containers with moisture absorbent material (silica gel). It is also recommended to return the unused LEDs to the original moisture proof package and to seal it again.

If the moisture absorbent material (silica gel) vaporizes or passes the expiration date, baking treatment should be performed by using the following conditions : 60°C for 20 hours.

The LED's electrode and lead frame comprise a silver plated copper alloy. The silver surface may be affected by environments. Please avoid conditions which may cause the LEDs to corrode or discolor. The corrosion or discoloration might lower solderability or affect optical characteristics.

Please avoid rapid transition in ambient temperature, especially in high humidity environments where condensation can occur.